

3.7 Algorithm Set Up Group

Introduction

This data deals with various algorithms in the controller: Control algorithm, Output algorithm, Current Duplex Range, and Relay Cycle Time Increment.

Function Prompts

Table 3-6 ALGOR Group (Numeric Code 500) Function Prompts

Prompt		Description	Selection or Range of Setting		Factory Setting
English	Numeric Code		Numeric Code	English	
CTRALG	501	Control Algorithm	0	ONOF	PIDA
			1	PIDA	
			2	PIDB	
			3	PDMR	
			4	TPSC (3 position step)	
OUTALG	502	Output Algorithm	0	RLY (Time simplex Relay 1)	depends on model
			1	RLY2 (Time simplex Relay 2)	
			2	CUR (Current simplex)	
			3	TPSC (3 Position step)	
			4	RLYD (Time duplex)	
			5	CURD (Current duplex)	
			6	CURT (Current/time duplex)	
			7	TCUR (Time/current duplex)	
4-20RG	503	Current Duplex Range	0	100 (Full)	100
			1	50 (Split)	
RLY TYP	504	Relay Cycle Time Increment	0	MECH (one sec. increments)	MECH
			1	S S (1/3 sec increments)	